



BUR920020144US1

Serial Number: 10/604,184

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**Date:** March 16, 2005

**Confirmation #:** 1183

**In re Application of:** Darren L. Anand et al.

**Filed:** 30-June-2003

**For:** Method and Circuit for Precise Timing of Signals in an Embedded  
Dram Array

**Serial Number:** 10/604,184

**Art Unit:** 2818

**Examiner:** David Lam

**RESPONSE AND AMENDMENT IN RESPONSE TO NOTICE OF NON-COMPLIANT  
AMENDMENT (37 CFR 1.121)**

Hon. Commissioner of Patents and Trademarks  
Alexandria, VA 22313

Sir:

This is in response to the Notice of Non-Compliant Amendment (37 CFR 1.121) mailed on 03/02/2005, a response being due by 04/02/2005. Applicants' request that this Amendment be entered in the above identified application and reconsideration of the application, in view of these comments, be made. Any fees required in entering this response may be charged to Applicant's deposit account, 09-0456.

The application should be amended as follows: